

Computing Minimal DNF of Boolean Functions for Digital Implementations

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Introduction

Modern communication systems impose strong constraints to digital circuit implementation of the Boolean function, such as

- reliability
- performance
- cost
- security

Notice

Constraints do not always appear in this order of importance.

A possible solution is to **minimize** the Boolean function **expression form**

$$f : \mathbb{B}^n \rightarrow \mathbb{B},$$

by which the number of gates is reduced.

Which form to choose for the minimization?

- **ANF** – Reed-Muller expansion

$$f(x_1, \dots, x_n) = \bigoplus_k \left(\bigwedge_{i_k=1}^{n_i} x_{i_k} \right), \quad n_i \leq n$$

- **CNF** – Product-Of-Sums

$$f(x_1, \dots, x_n) = \bigwedge_k \left(\bigvee_{i_k=1}^n \xi_{i_k} \right), \quad \xi_i \in \{x_i, \bar{x}_i\}$$

- **DNF** – Sum-Of-Products

$$f(x_1, \dots, x_n) = \bigvee_k \left(\bigwedge_{i_k=1}^n \xi_{i_k} \right), \quad \xi_i \in \{x_i, \bar{x}_i\}$$

What is the minimal form of DNF?

The conjunction term p containing *some* literals

$$p = \wedge \xi_i, i = 1, \dots, k \leq n$$

having no sub terms

$$q \subset p : q(\mathbf{x}) = 1 \implies f(\mathbf{x}) = 1$$

is called *prime implicants*.

Minimal DNF

The DNF with a minimum number of *prime implicants*.

Such a minimal DNF form, being implemented in the digital circuit, is **optimal** in the restricted class of two-level digital circuits (disjunctions of conjunctions of literals) gates [Weg91].

Why DNF is selected?

NAND implementation

It is easy to implement a Boolean function with NAND gates only if converted from a DNF form.

For example, $f(\mathbf{x}) = (x_1 \wedge x_2) \vee (x_3 \wedge x_4) = \overline{\overline{(x_1 \wedge x_2)} \wedge \overline{(x_3 \wedge x_4)}}$

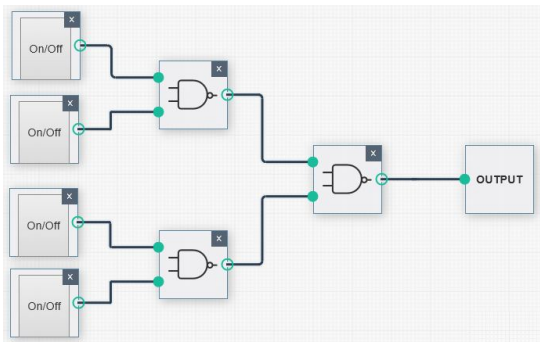


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How to find a minimal DNF of the Boolean function?

- Karnaugh maps – expression simplification; appropriate for functions defined on less than ≈ 6 variables
- Minterms (Quine-McClusky [Weg91]) – conjunction term simplification; appropriate for functions defined on less than ≈ 15 variables
- Heuristic (Espresso [MSBS93]) – prime implicants heuristic search; appropriate for functions with less than ≈ 50 variables
- BDDs (Coudert [CM94]) – set cover implicit computation; applicable for functions with ≈ 100 variables or more

Monotonic Boolean functions

Minimal DNF of monotonic functions can be computed with BDDs according to the Rauzy approach [Rau93].

What are BDDs?

One of the only really fundamental data structures that came out in the last twenty-five years (D.E. Knuth, 2008)

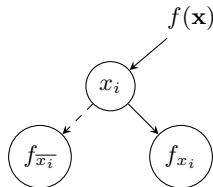
Bryant [Bry86]

The Boolean Decision Diagrams (BDDs) a variant of *directed acyclic graphs* (DAGs) used for Boolean functions representation.

Two-terminal DAG based on Shannon identity

$$f(\mathbf{x}) = (x_i \wedge f_{x_i}(\mathbf{x})) \vee (\overline{x_i} \wedge f_{\overline{x_i}}(\mathbf{x}))$$

built from vertices representing the *If-Then-Else* (ITE) construct.



Why and when BDDs?

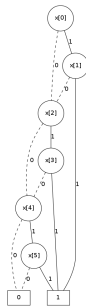
Canonicity

The BDDs are a **canonical** representation of the Boolean function.

but, they are **sensitive** to the selection of the variable order



Bad order¹



Good order

¹Source: PyEDA documentation [Dra20]

Any strong results for BDDs? I

Hardness of variable ordering

- The problem of finding an optimal order is *NP-hard* [TY00], and even by improving a variable order, the problem is *NP-complete* [BW96],
- There are Boolean functions which have an exponential size BDD for every ordering [Bry86]

Friedman-Supowit [FS90]

Let $I \subseteq \{1, \dots, n\}$, $k = |I|$, $v \in I$, then there is a **constant** c such that for each $\pi \in \Pi(I)$ satisfying $\pi[k] = v$, we have

$$\text{cost}_v(f, \pi) = c.$$

Any strong results for BDDs? II

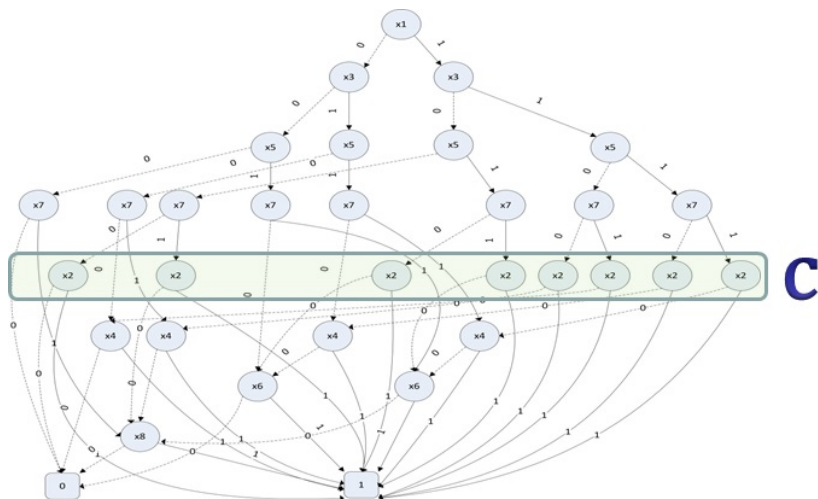


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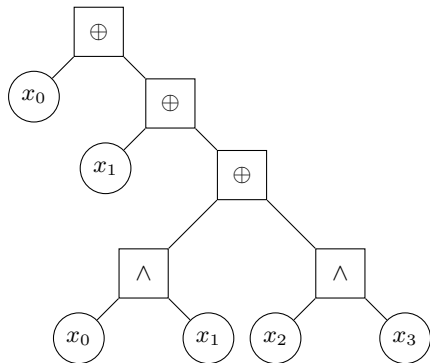
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Parse tree

Parse tree

First step: to build the parse tree of the Boolean function expression

Option: to build a Boolean Expression Diagram (BED) to remove redundant subexpressions [AH02]



$$f(\mathbf{x}) = x_0 \oplus x_1 \oplus (x_0 \wedge x_1) \oplus (x_2 \wedge x_3)$$

A new variable ordering heuristic

Input: f top node in parse tree or BED

Output: π a new variable order for top node

Orders $\leftarrow \emptyset$; // Hash table for orders

for each node \in $DFSOrder(f)$ **do**

if node is Variable **then**

$x_i \leftarrow \text{Variable}(\text{node})$

$\pi_i \leftarrow \{(x_i, \text{count} : 1)\}$

 Orders.Insert(key:node, value: π_i)

end

else

 /* node is boolean operator */

$\pi_l \leftarrow \text{Orders.FindValue}(\text{key:LeftChild}(\text{node}))$

$\pi_r \leftarrow \text{Orders.FindValue}(\text{key:RightChild}(\text{node}))$

$\pi \leftarrow \text{MergeOrders}(\pi_l, \pi_r)$ // Merge heuristic

 Orders.Insert(key:node, value: π)

end

end

$\pi \leftarrow \text{Orders.FindValue}(\text{key}:f)$

return π // final order of variables for f node

MergeOrders(π_l, π_r)

Input: π_l, π_r two variable orders for merging

$\pi \leftarrow \emptyset$

$(x_l, c_l) \leftarrow \text{First}(\pi_l)$

$(x_r, c_r) \leftarrow \text{First}(\pi_r)$

while $(x_l \neq \emptyset) \& (x_r \neq \emptyset)$ **do**

 /* Heuristic criteria */

if $(c_l + 2 * c_r) \leq (c_r + 2 * c_l)$ **then**

 Append_If_Not_Present($\pi, \{x_l, \text{count} : c_l + 2 * c_r\}$)

$(x_l, c_l) \leftarrow \text{Next}(\pi_l)$

end

else

 Append_If_Not_Present($\pi, \{x_r, \text{count} : c_r + 2 * c_l\}$)

$(x_r, c_r) \leftarrow \text{Next}(\pi_r)$

end

end

if $x_l \neq \emptyset$ **then**

 Append($\pi, \text{from}:x_l, \pi_l$) // Left finished?

end

if $x_r \neq \emptyset$ **then**

 Append($\pi, \text{from}:x_r, \pi_r$) // Right finished?

end

return π // Two orders merged

Results

Table: Preliminary results

example	variables	operators	$ BDD $ ¹	$ DNF $ ²
<i>ex1</i>	13	12	15	11
<i>ex2</i>	70	53	254	36.292
<i>ex3</i>	43	32	57	1.043
<i>ex4</i>	66	50	281	32.369
<i>ex5</i>	44	35	49	784
<i>ex6</i>	98	141	264	960
<i>ex7</i>	18	19	31	46
<i>ex8</i>	61	84	2.481	46.188
<i>ex9</i>	194	158	3.048	34.477.555

¹ count of nodes in BDD built with heuristic order

² count of prime implicants for minimal DNF form

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Why minimal DNF and BDDs?

Some reasons for minimal DNF in digital implementations





- Reduced cost due to the minimal number of gates used
- Improved performance due to minimized total delay
- Design constraints fulfilled (die size, thermal, ...)
- More reliable circuits

What else can be done with BDDs besides the DNF minimization?





- Expressing (and solving) of a 0/1 optimization problems
- Representing a structure function of complex systems
- Could properties of the Boolean functions be derived from topological properties of the BDD DAGs
- Other ideas?

Thank You!



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